

# BerryNOS 1543

**user manual**

Revision 1.0



# Overview

BerryNOS 1543 is an 16-bit stereo audio reference DAC module designed for Raspberry PI model B. I<sup>2</sup>S based together with Philips D/A converter with a fully balanced design and a unique discrete buffered output stage.

BerryNOS 1543 has S/PDIF input as an extra option, which makes it a DAC module that can be used with basically any audio source.

## Power Requirements

External PSU needed:  
2x7.5-9VAC 25-40VA (DAC)  
5VDC (Raspberry PI)

## Configuration (optional S/PDIF input)

BerryNOS 1543 is easy configured with an onboard jumper for different output frequencies when S/PDIF input is used. Either no resampling up to 192 kHz or fixed resampling at 176.4 kHz.

## Technical details

Input up to 24bit  
Output up to 96 kHz  
Output 4.7  $\Omega$  balanced  
Output 4.7  $\Omega$  unbalanced  
Frequency 4 Hz - 18 kHz, +0 -0.3dB  
THD+N = 0.02% (1 kHz)

## Inputs

Raspberry PI input

Optional:  
S/PDIF input

## Outputs

L/R Balanced XLR  
L/R Unbalanced RCA

## Optional

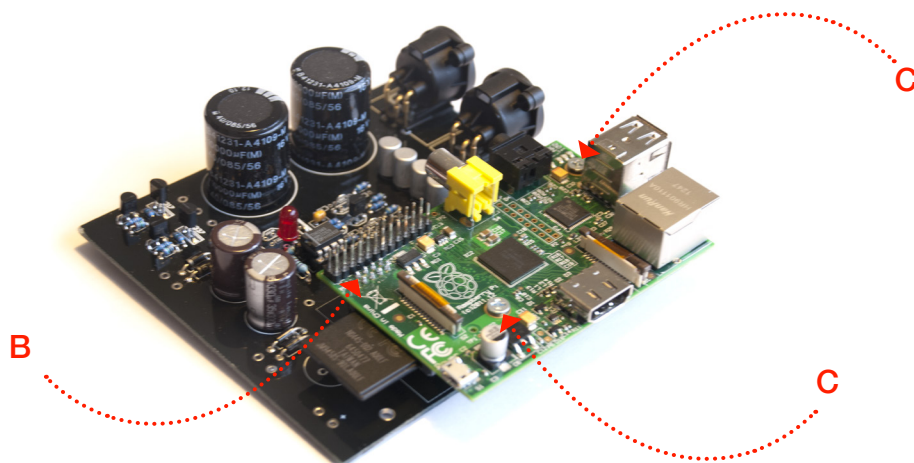
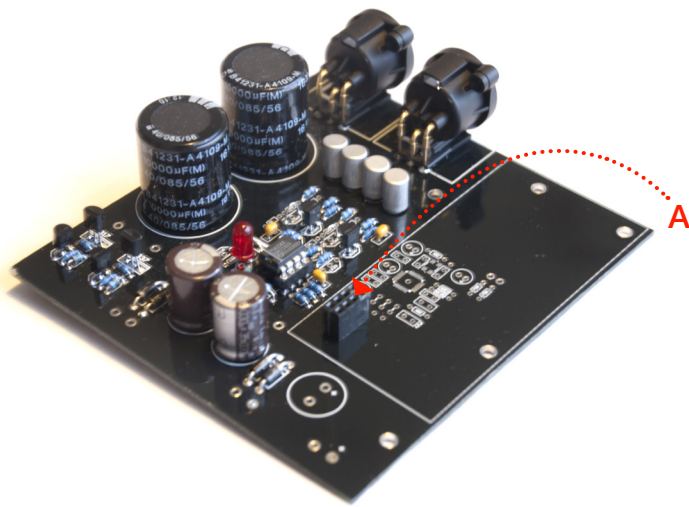
S/PDIF input



# Installation with Raspberry PI

BerryNOS 1543 is easy installed within seconds if you follow this guide.

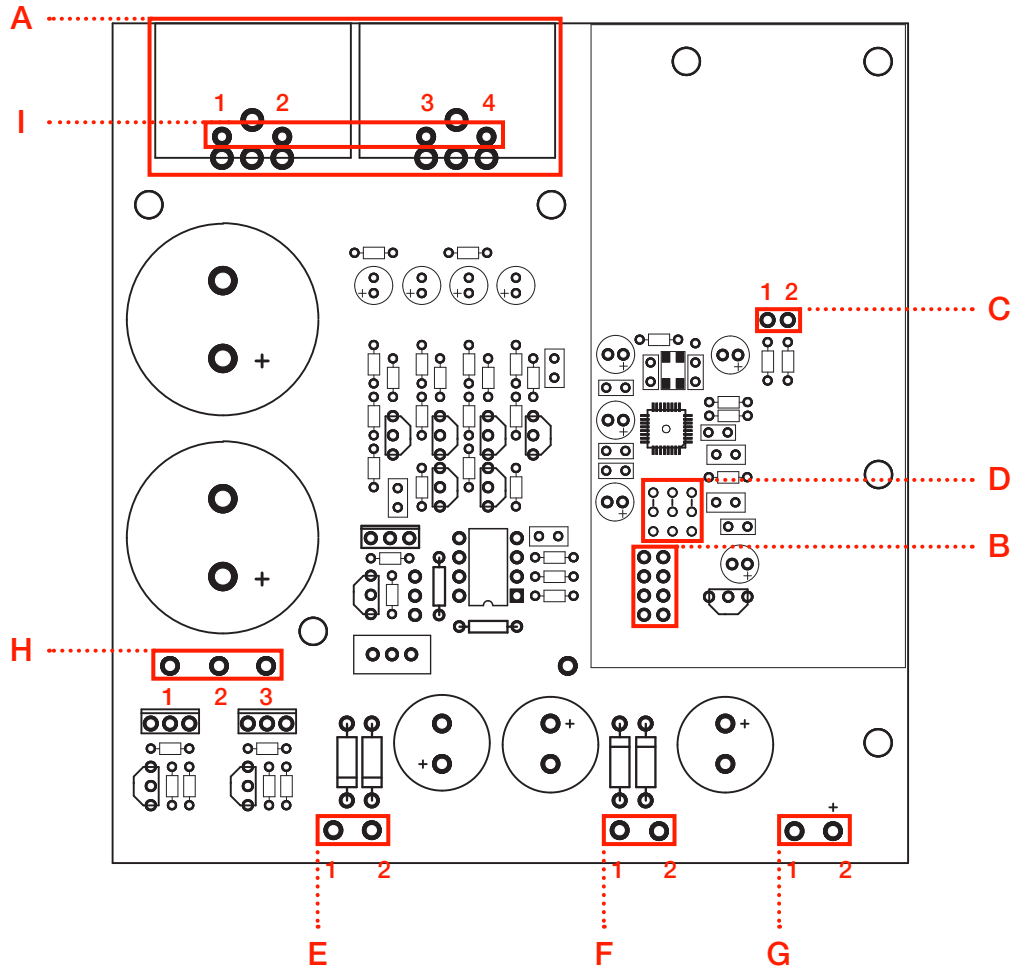
- A** Attach the BerryNOS 1543 female header to Raspberry PI male header.
- B** Notice Raspberry PI header P5 should be soldered on the bottom. Otherwise BerryNOS 1543 will not work and the screw installation later on will not fit.
- C** Insert the short spacers between BerryNOS 1543 and Raspberry PI and tighten the screws through BerryNOS 1543-Spacers-Raspberry PI.



# PCB Layout


Overview of BerryNOS 1543 PCB layout.

- |          |                                      |          |       |
|----------|--------------------------------------|----------|-------|
| <b>A</b> | L/R Balanced XLR, L/R Unbalanced RCA | <b>F</b> | CON_2 |
| <b>B</b> | Raspberry PI header P5               | <b>G</b> | CON_3 |
| <b>C</b> | Optional S/PDIF input                | <b>H</b> | CON_4 |
| <b>D</b> | Optional I2S configuration           | <b>I</b> | CON_5 |
| <b>E</b> | CON_1                                |          |       |





**CON\_1**  
 Pin 1 0VAC  
 Pin 2 7.5-9VAC



**CON\_2**  
 Pin 1 7.5-9VAC  
 Pin 2 0VAC


**CON\_3**  
 Pin 1 GROUND  
 Pin 2 5VDC


**CON\_4**  
 Pin 1 +7.2VDC  
 Pin 2 -7.2VDC  
 Pin 3 0VDC

**CON\_5** (Note! Top view)  
 Pin 1 RCA L+ SIGNAL  
 Pin 2 RCA L GND  
 Pin 3 RCA R+ SIGNAL  
 Pin 4 RCA R GND


**Optional S/PDIF input**  
 This is an alternative input instead of Raspberry PI.  
 Pin 1 S/PDIF input  
 Pin 2 GROUND


**Optional I2S configuration for S/PDIF**  
 Configure with provided onboard jumpers.  
 Connect according to blue schematic for no resampling up to 192 kHz.  
 Connect according to green schematic for fixed resampling at 176.4 kHz

